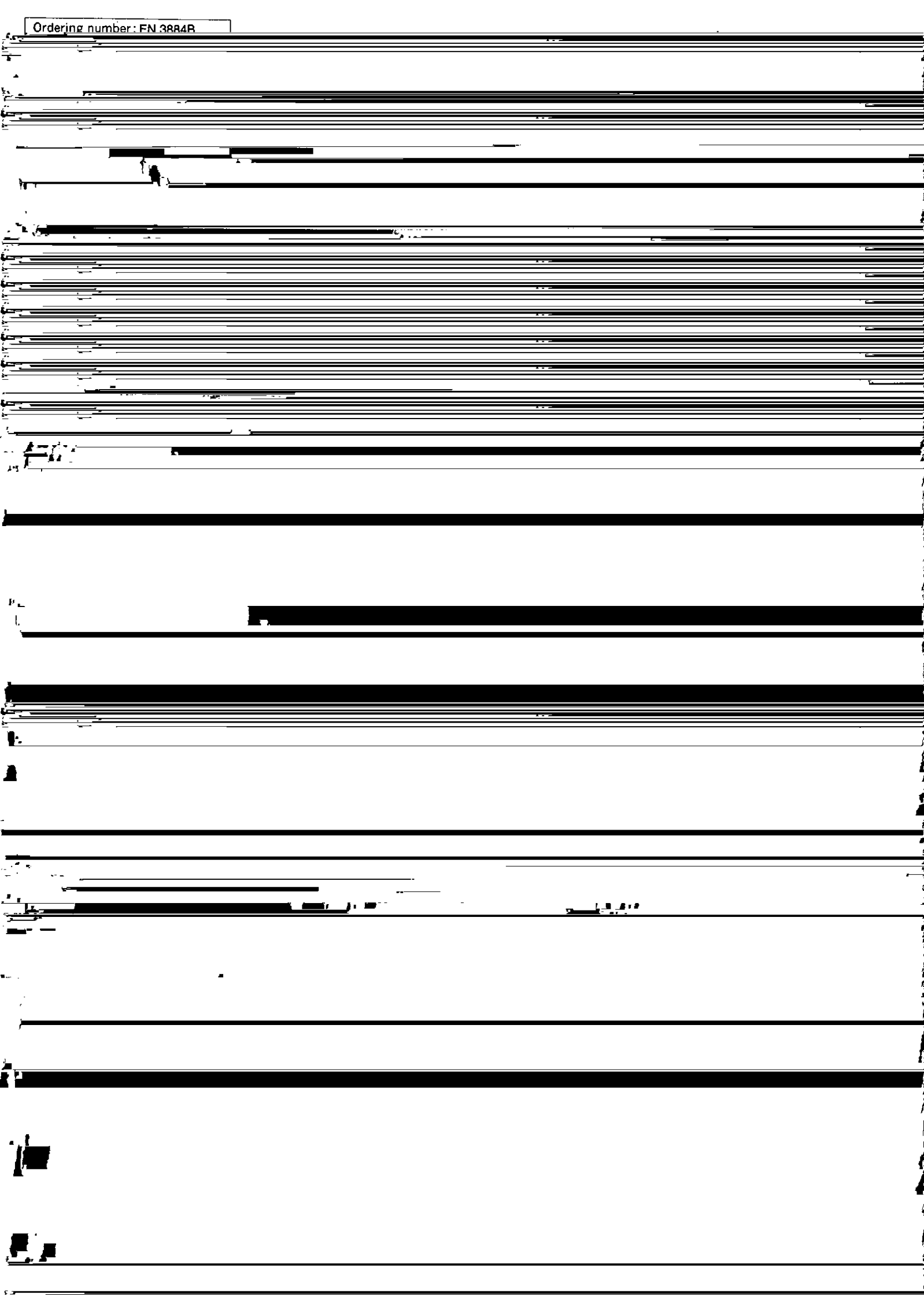




LC7883K,7883KM

Package Dimensions

3012A-DIP28

Block Diagram



LC7883K,7883KM

Number	Name	Function
22	DGND	Digital ground

LC7883K,7883KM

Parameter	Symbol	Conditions	Ratings	Unit
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Input Data Format

Serial data is input in 2s complement format with the most significant bit (msb) first. Control data is input

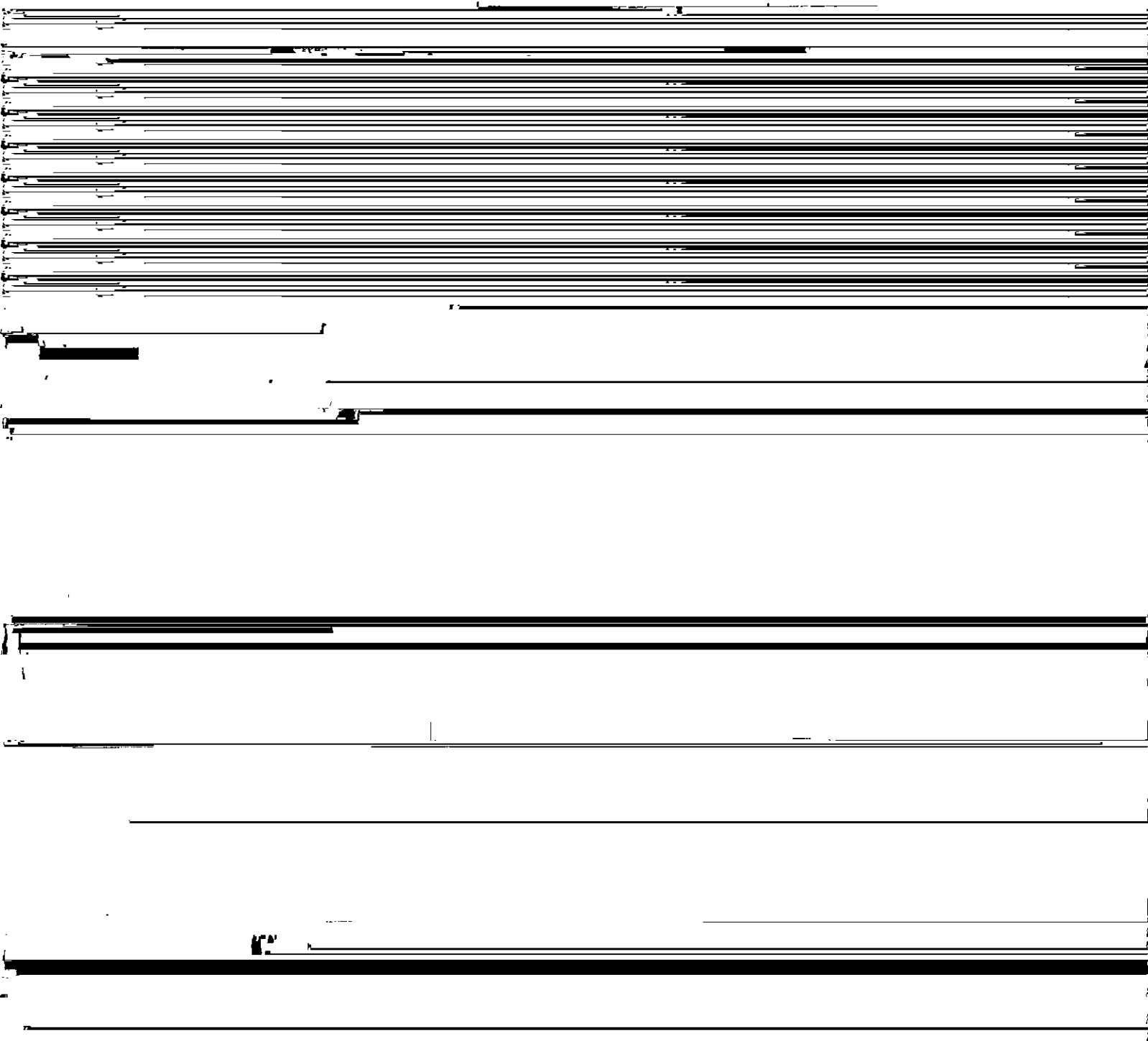


Figure 6. Initialization

LC7883K,7883KM

Normal rate is selected when A1 is LOW, and double rate, when A1 is HIGH. Deemphasis filtering is selected using A2 and A3 as shown in table 3.

Table 3. Deemphasis filtering

A2	A3	Deemphasis	Sampling rate
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LC7883K,7883KM

Note

The digital-to-analog converters have high impedance outputs which can be matched using emitter follower

op-amps. The TEST pins are normally tied LOW and INTR HIGH

